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Cathi H. Turner  
Date of Signature

Cathi H. Turner  
October 28, 2003

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hänsel et al.

Group Art Unit: Not Assigned

Serial No.: 10/660,091

Examiner: Not Assigned

Filed: September 10, 2003

Docket No. 1406/166

Confirmation No.: Not Assigned

For: FABRICATION METHOD FOR A SEMICONDUCTOR STRUCTURE  
HAVING A PARTIALLY FILLED TRENCH

\* \* \* \* \*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.56, 1.97, and 1.98, applicants' undersigned attorney brings to the attention of the Patent and Trademark Office the documents listed on the attached Form PTO-1449. Copies of the references as well as Form PTO-1449 are attached hereto. This is not to be construed as a representation that a search has been made or that a reference is relevant merely because cited.

Early passage of the subject application to issue is earnestly solicited.

Serial No.: 10/660,091

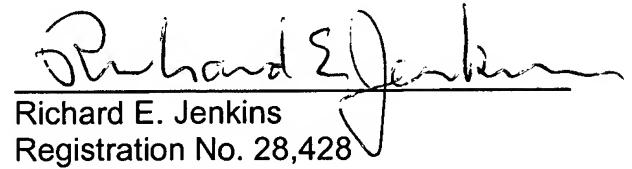
Although it is believed that no fee is due, the Commissioner is hereby authorized to charge any fees associated with the filing of this Information Disclosure Statement to Deposit Account No. 50-0426.

Respectfully submitted,

JENKINS, WILSON & TAYLOR, P.A.

Date: 10-25-03

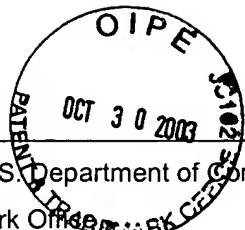
By:

  
Richard E. Jenkins  
Registration No. 28,428

REJ/cht

Enclosures

Customer No: 25297



FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office				Attorney Docket No.: 1406/166	Serial No.: 10/660,091		
List of Documents Cited by Applicant							
				Applicant(s): Hänsel et al.			
				Filing Date: September 10, 2003		Group:	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial	No.	Document Number	Date	Name	Class	Subclass	Filing date if Appropriate
	01	US2002/006 6884 A1	06/06/2002	TSAI et al.	252	79.1	
	02	6,284,665 B1	09/04/2001	Lill et al.	438	710	
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Name of Patentee or Applicant		Translation Yes   No
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							

EXAMINER \_\_\_\_\_

DATE CONSIDERED \_\_\_\_\_

\*Examiner Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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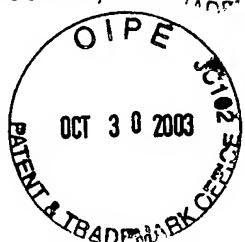
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October 28, 2003

OCT 30 2003



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October 28, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: U.S. Patent Application Serial No. 10/660,091 for  
FABRICATION METHOD FOR A SEMICONDUCTOR  
STRUCTURE HAVING A PARTIALLY FILLED TRENCH  
Our Ref. No. 1406/166

Sir:

Please find enclosed in connection with the subject U.S. patent application the following documents:

1. Information Disclosure Statement (1 page);
2. Form 1449 (1 page) (duplicate);
3. Copies of cited references (2 references); and
4. A return-receipt postcard to be returned to us with the U.S. Patent and Trademark Office filing stamp thereon.

The Commissioner is hereby authorized to charge any fees associated with the filing of this correspondence to Deposit Account No. 50-0426.

Respectfully submitted,

JENKINS, WILSON & TAYLOR, P.A.

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